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Apparatus and Method for Measuring Received Signal Strength Indicator, and Recording ...

Cl Yeh, HS Lim, DS Kwon - US Patent App. 10/588.101, 2004 - Google Patents ... 3, the RSSI estimation apparatus includes a receive antenna 100, an ADC 200, an ABS 300, an accumulate unit 400, a square unit 500, and a multiply unit 600. ... Alf 4 versions

Wireless receiver and method of wireless reception

T Toda - US Patent App. 09/979,085, 2002 - Google Patents ... f (/, m) (17) [0063] In this case, the multiplication section 106 multi-plies each ... A division section 302 divides a measured RSSI by the square of the sum ... All 6 versions

IPDFI *Parameterization of algorithms and fpga accelerators to predict performance

C Steffen - Reconfigurable System Summer Institute (RSSI), 2007 - rssi.ncsa.uiuc.edu

... for(i=0;i<SIZE;i++){ dot_prod += (A[i]*B[i]); } The second algorithm, "algorithm

B", is a simple square matrix multiply. The code is a triply-nested loop. ...

Cited by 2 - Related articles - View as HTML

Received signal strength indicator utilizing approximation factors and the I and Q signal ...

MP Gabato, MA Gannon - US Patent 5,603,112, 1997 - Google Patents

... second ROM is required to calculate the square-root (or ... 5 is a flowchart showing

RSSI calculations, as performed by ... 513 ZI=X-(vie)X, where the multiply by Vie ...

Cited by 3 - Related articles - All 2 versions

An Elementary Realization of a Nondiscrete Factor

AY Helenskii - RUSSIAN JOURNAL OF MATHEMATICAL PHYSICS, 2000 - maik.ru

... of square-integrable functions on the exotic square is an ... algebra with respect to

our multiplication and a ... with this server, contact webmaster@maik.rssi.ru.

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A 900 MHz transceiver chip set for dual-mode cellular radio mobileterminals

IA Koullias, JH Havens, IG Post, PE Bronner - 1993 IEEE International Solid-State Circuits 1993 - ieeexplore.ieee. org

... Since the mixers multiply the baseband signals with a square-wave LO signal, Fourier analysis shows that the ... and a received signal strength indicator (RSSI). ...

Cited by 30 - Related articles - Bt. Direct

Circuit design techniques for very low-voltage analog functionalblocks using triple-tail cells

K Kimura - IEEE Transactions on Circuits and Systems I; ..., 1995 - leeexplore.leee.org ... breadboard, 1. INTRODUCTION HE LINEAR transconductance amplifier (ie, an

OTA) T and the square-law transconductance amplifier (ie, a ...

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[PDF] *A High Performance FPGA-Based Accelerator for BLAS Library Implementation

S Rousseaux, D Hubaux, P Guisset, JD Legat - ... Reconfigurable Systems Summer Institute 2007, RSSI, 2007 - rssi. nosa uiuc.edu

... Va = 1 • BLAS level 2: vector-matrix multiplication: O = 2 x n2, Ma = nx (n+1) Va =

(2xn)/(n+1) • BLAS level 3: matrix multiplication of square matrixes of ...

Cited by 1 - Related articles - View as HTML - All 2 versions

Diverse antenna system

BG Goldberg - US Patent App. 10/966,281, 2004 - Google Patents

... 69 has an amplification circuit, which provides an RSSI (power level ... particularly,

it is scaled to the square of the ... of signal 74, and 2) to multiply the signal ...

All 2 versions

Digital post-detection FM spatial diversity combination circuit

JD Kaewell Jr. DM Cooley, Y Kim - US Patent 5,402,451, 1995 - Google Patents

... means to achieve this is to square the RSSI ... Since the RSSI readings are not very useful, they ... third and fourth multipliers for respectively multiply- ing said ... Cited by 4 - Related articles - All 2 versions



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